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PATENT APPLICATION

ATTORNEY DOCKET NO. 10980239-1

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IN THE U.S. PATENT AND TRADEMARK OFFICE
Patent Application Transmittal Letter

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Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 CFR 1.53(b) is a(n): ☒ Utility () Design
☒ original patent application,
() continuation-in-part application

INVENTOR(S): James R. Sheats et al.

TITLE: METHOD OF MANUFACTURE OF ACTIVE MATRIX ADDRESSED POLYMER LED DISPLAY

Enclosed are:

- ☒ The Declaration and Power of Attorney. ☒ signed () unsigned or partially signed
☒ 3 sheets of drawings (one set)
() Information Disclosure Statement and Form PTO-1449 () Associate Power of Attorney
() Priority document(s) () (Other) (fee \$)

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(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) TOTALS
TOTAL CLAIMS	12 — 20	0	X \$ 22	\$ 0
INDEPENDENT CLAIMS	2 — 3	0	X \$ 82	\$ 0
ANY MULTIPLE DEPENDENT CLAIMS	0		\$ 270	\$ 0
BASIC FEE: Design (\$330.00); Utility (\$790.00)				\$ 790
TOTAL FILING FEE				\$ 790
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Respectfully submitted,

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Active Matrix Addressed Polymer LED Display

Field of the Invention

5 The present invention relates to display devices, and more particularly, to information displays using organic electroluminescent materials and thin film field effect transistors .

Background of the Invention

10 Active matrix liquid crystal displays for computers and the like provide significantly improved performance; however, the cost of such displays is also a significant factor in the overall price of portable computers using the same. In addition, active matrix displays require substantially more power than passive displays since this type of display utilizes backlighting.

15 An active matrix liquid crystal display is constructed from two components that are separately constructed and then bonded together. The first component consists of an array of thin film transistors (TFTs) fabricated on a glass substrate, one per pixel. Each TFT has an associated capacitor which stores a potential value which is then applied to a liquid crystal
20 element which passes a fraction of the light incident thereon, depending on the applied voltage. The TFTs are connected by transparent row and column electrodes, which are used to select each pixel. The liquid crystal layer together with color filters are fabricated on a second glass substrate. An adhesive is screen-printed around the edge of one glass plate, and an optical aligner is then used to bring the two together with the TFTs aligned to the filters.
25 The adhesive is then UV (ultraviolet) cured and the liquid crystal material introduced by capillary action. The polarizers and a backlight are attached to the outside of the display.

30 Displays based on organic light emitting devices (OLEDs) have the potential for substantially reducing the cost of computer displays. OLEDs are emissive displays that provide an alternative to other types of light emission, such as vacuum fluorescence, plasma, inorganic electroluminescence, and inorganic light emitting diodes. Since a pixel only draws

power when "on", a display based on OLEDs requires less power than a backlight-based display.

OLEDs are constructed on a transparent substrate coated with a transparent
 5 conducting material, such as Indium Tin oxide (ITO), one or more organic layers and a
 cathode made by evaporating or sputtering a metal of low work function characteristics, such
 as Ca or Mg. The organic layers are chosen so as to provide charge injection and transport
 from both electrodes to the electroluminescent organic layer (EL) where the charges
 recombine emitting light. Often there is at least one or two organic hole transport layers
 10 (HTL) between the ITO and the EL, as well as at least one or two electron injection and
 transporting layers (EL) between the cathode and the EL.

While OLEDs have the potential for reducing display costs, prior art methods for
 fabricating OLEDs have several disadvantages when applied to active matrix displays. In
 15 principle, a set of TFTs and conductors on a substrate can also be utilized for constructing an
 active matrix display based on OLEDs. After constructing the TFTs the pixels are defined by
 depositing a patterned conductor corresponding to either the cathode or anode of the OLED.
 The organic light emitting layers and associated transport layers are then deposited, followed
 by a uniform layer of the other OLED electrode (anode or cathode). Such structures are
 20 described in detail in Eugene Y. Ma, et al., *Soc. Inform. Display Conf. Proc.* Sept. 15-19,
 1997 (Toronto, Canada), p.L78.

While such a process has the potential to produce useful displays which may be
 competitive with AMLCDs, it has several disadvantages. First, the processing of the organic
 25 LED must be done on the same substrate as used for the TFTs, typically glass. As a result,
 the large cost advantages associated with fabricating polymer LEDs on flexible plastic
 substrates with roll-to-roll equipment cannot be realized. Ma, *et al.*, attempt to overcome this
 limitation by using stainless steel foil as a substrate. In this method, the TFTs and OLEDs are
 constructed as described in the previous paragraph, using stainless steel foil in place of glass.
 30 However, since this substrate is opaque, the top electrode of the OLED must be transparent.
 The preferred transparent electrode material is indium tin oxide (ITO) which is deposited by
 sputtering. Hence, the method suggested by Ma, *et al.* requires the active organic layers to be

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Summary of the Invention

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terminals. In this embodiment, the isolation transistor connects the storage capacitor to the bit line when the isolation transistor is placed in the conducting state by the application of the logic signal to the word line. The OLEDs are part of an array of OLEDs. The array of OLEDs is constructed on a flexible sheet having first and second surfaces, the first and second surfaces being parallel to one another, the flexible sheet being transparent to light of a first wavelength. A first electrode layer is in contact with the first surface, the first electrode layer being transparent to light of the first wavelength. A light emitting layer including an organic polymer is in electrical contact with the electrode layer. A plurality of second electrodes, one such second electrode corresponding to each of the OLEDs, is in electrical contact with the light emitting layer. Each second electrode has an isolated conducting area in electrical contact with the light emitting layer, the light emitting layer generating light of the first wavelength in a region adjacent to the second electrode when a potential difference is applied across the first and second electrodes. The driving transistors are part of a transistor array having a plurality of connection points disposed on a surface, each connection point corresponding to one of the second electrodes in the array of OLEDs. The connection points are arranged such that each second electrode overlies the corresponding connection point when the array of OLEDs is properly aligned with the transistor array. The display includes a bonding layer located between the transistor array and the array of transistors, the bonding layer electrically connecting each of the second electrodes to that second electrodes corresponding connection point.

Brief Description of the Drawings

Figure 1 is a schematic drawing of a portion of a display according to the present invention.

Figure 2 is a top view of an OLED array according to the present invention.

Figure 3 is a cross-sectional view of the array shown in Figure 2 through line 51-52.

Figure 4 is a cross-sectional view of a portion of a display according to the present invention illustrating the connection of the OLEDs to the driving transistors.

Detailed Description of the Invention

The present invention overcomes the problems associated with the above described prior art methods by utilizing a laminating technique that allows an OLED-based sheet of light emitting diodes formed on a flexible plastic substrate to be laminated onto a solid TFT substrate. In this manner, the cost advantages associated with OLEDs are preserved.

Refer now to Figure 1 which is a schematic drawing of a portion of a display 10 according to the present invention. The portion of display 10 shown in Figure 1 consists of four pixels 20, 25, 30, and 35. Each pixel consists of two TFT transistors, an OLED, and a capacitor for storing the intensity to be displayed by the OLED. Referring to pixel 20, the current flowing through OLED 24 is determined by the voltage on the gate of TFT 22 and the supply voltage provided on bus 15. The voltage on the gate of TFT 22 is set by applying the desired voltage to a bit line 14 while placing gate TFT 21 in a conducting state. The voltage programs capacitor 23 with the voltage on the bit line.

The pixels are written in "words". All of the pixels connected to a particular word line are written in parallel by applying the desired programming voltages to the bit lines. Pixels 20 and 30 are programmed through bit line 14, and pixels 25 and 35 are programmed through bit line 16. The word selected for programming is selected by applying an appropriate logic voltage on a corresponding word line. Pixels 20 and 25 are selected via word line 12, pixels 30 and 35 are selected via word line 13.

A display according to the present invention is constructed by bonding an array of OLEDs to an array of TFTs. To simplify the following discussion, the fabrication of an array of OLEDs for a monochrome display will be described first. The manner in which a color display is fabricated will be discussed in more detail below.

Refer now to Figures 2 and 3 which illustrate an array of OLEDs 50 according to the present invention. Figure 2 is a top view of array 50, and Figure 3 is a cross-sectional view of array 50 through line 51-52 shown in Figure 2. To clarify the various layers that make up

array 50, the layers have been shown displaced from one another at the edges of the array. An array is constructed on a thin plastic substrate 61 constructed from a transparent polymer that is capable of withstanding the processing temperatures involved in depositing the various layers. The processing temperatures are typically less than 150°C. Substrates constructed from poly(ethylene terephthalate), or PET may be used. A transparent conducting layer 62 is deposited on the substrate. The preferred conducting material is ITO. Layer 62 becomes a common anode electrode which is shared by all of the OLEDs. The various polymer layers are then deposited on the anode electrode. These include any hole and electron transport layers as well as the electroluminescent layer. To simplify the drawing, these layers are shown as a single layer 63. Finally, a cathode layer is deposited and patterned to form individual cathode electrodes 64. By applying a voltage between a cathode electrode and the common anode electrode, the portion of the light emitting layer 62 under that cathode electrode is caused to emit light that escapes through the transparent plastic substrate 61.

Array 50 is bonded to a substrate on which the TFTs and associated capacitors have already been fabricated utilizing an anisotropic conductive adhesive (ACA), which serves both to hold the two devices together and to make the necessary electrical contacts. ACAs suitable for this purpose are well known and are manufactured by Hitachi, 3M Corp., and Alpha metals, Inc. These adhesives are described in "Flip Chip Technologies", ed. John Lau (McGraw-Hill, 1996). Referring to Figure 4, which is a cross-sectional view of a portion of a display 80 containing one pixel that is controlled by a TFT 71. TFT 71 is constructed on a glass substrate 76 having a SiN insulating gate oxide layer 73 which isolates gate 72 from a polysilicon layer 74. The drain and source of TFT 71 are shown at 75 and 77, respectively. The source electrode 77 is covered by an insulating layer 78.

TFT 71 is connected to the corresponding OLED by a layer of ACA 81 which bonds the drain 75 to the cathode 64 of the OLED. The electrical conduction is provided by compressible conducting particles 82 in an electrically insulating glue material. The array of OLEDs is pressed onto the array of TFTs to form the various contacts. Hence, it is advantageous to have an array of OLEDs that is constructed on a flexible substrate, as this allows sufficient flexing of the OLED array to assure that each OLED is connected to its corresponding TFT. Alignment of the two substrates is accomplished by an optical aligner of

the type that is currently used for aligning the standard liquid crystal cells described above. The TFT substrate provides a solid barrier against permeation of water or oxygen into the OLED, leaving a requirement of only a single barrier 88.

5 The above described embodiment of the present invention was a monochromatic display. That is, all of the OLEDs generate the same wavelength light. Color displays require that pixels generate different color spectra. Typically, a color pixel is constructed from three pixels having red, green, and blue emitters.

10 Multicolor pixels may be provided by two approaches. The first approach consists of depositing emitting materials with different spectral characteristics. Each of these materials (typically one material with red emission, one for green and one for blue) is deposited separately in different parts of the substrate to achieve "full color" (RGB) pixels by separately powering the three color pixels. This technique requires the patterning of the
15 electroluminescent layer 63 discussed above. Methods for patterning such layers are known in the art, and hence, will not be discussed in detail here.

20 The second approach consists of depositing a single emissive material and using (RGB) filters, resonant cavities and/or photoluminescent materials that can absorb the light from the emissive material and re-emit light at longer wavelengths (green and red). This approach requires the emissive material to have a substantial emission in the blue. Such wavelength converters are known in the art, and hence, will not be discussed in detail. The reader is directed to U.S. Patents 5,121,214 and 5,294,820 for a discussion of such materials.

25 Devices based on color conversion are preferred because it is much easier to pattern the wavelength converters than electroluminescent materials. In addition, these devices do not require that the electroluminescent characteristics of three different emissive materials be balanced. In general, the voltage-current characteristics and quantum efficiencies are usually very different for each material. The lifetimes of the electroluminescent materials are also
30 quite difficult to equalize between different materials.

In the preferred embodiment of the present invention, the color conversion materials are deposited on the opposite side of the plastic substrate from the ITO layer. Such a light converter is shown in Figure 4 at 89.

While the above described embodiments of the present invention utilize a layer of an anisotropic conductive adhesive to bond the array of OLEDs to the array of TFTs, other bonding arrangements may be utilized. For example, a patterned conductive adhesive can be applied using screen printing or precision stencil printing techniques. Such adhesives are described in D. Durand, *et al*, "Electrically Conductive Cement Containing Agglomerate, Flake and Powder Metal Fillers", U.S. Patent 5,180,523 and in K. Gilleo, "Polymer Bonding Systems Offer Alternatives to Soldering", Electronic Packaging and Production, December 1992, pp. 52-54. These techniques are used in printed circuit fabrication to apply conductive adhesives, and hence will not be described in detail here. The anisotropic conductive adhesive described above is preferred because no patterning or mask step is needed.

Various modifications to the present invention will become apparent to those skilled in the art from the foregoing description and accompanying drawings. Accordingly, the present invention is to be limited solely by the scope of the following claims.

WHAT IS CLAIMED IS:

1. A display comprising a plurality of light emitting pixels, each pixels comprising an isolation transistor, a driving circuit, and an organic light emitting diode (OLED), said driving circuit storing a value that determines the magnitude of the light emitted by that pixels, said driving circuit placing said OLED in a conducting path between first and second power terminals, said isolation transistor connecting said driving circuit to a bit line when said isolation transistor is placed in a conducting state by the application of a logic signal to a word line.

2. The display of Claim 1 wherein said driving circuit comprises a storage capacitor and a driving transistor, said storage capacitor storing a charge that determines the magnitude of the light emitted by said pixels, said driving transistor having a gate connected to said storage capacitor, said driving transistor connecting said OLED between said first and second power terminals, said isolation transistor connecting said storage capacitor to said bit line when said isolation transistor is placed in said conducting state by the application of said logic signal to said word line.

3. The display of Claim 2 wherein said OLEDs are part of an array of OLEDs, said array of OLEDs comprising:

a flexible sheet having first and second surfaces, said first and second surfaces being parallel to one another, said flexible sheet being transparent to light of a first wavelength;

a first electrode comprising a first electrode layer in contact with said first surface, said first electrode layer being transparent to light of said first wavelength;

a light emitting layer comprising an organic polymer in electrical contact with said first electrode layer; and

a plurality of second electrodes, one such second electrode corresponding to each of said OLEDs, each of said second electrodes comprising an isolated conducting area in

electrical contact with said light emitting layer, said light emitting layer generating light of said first wavelength in a region adjacent to said second electrode when a potential difference is applied across said first and second electrodes.

5 4. The display of Claim 3 wherein said driving transistor are part of a transistor array having a plurality of connection points disposed on a surface, each of said connection points corresponding to one of said second electrodes in said array of OLEDs, said connection points being arranged such that each second electrode overlies said corresponding connection point when said array of OLEDs is properly aligned with said transistor array, and wherein
10 said display further comprises a bonding layer located between said transistor array and said array of OLEDs, said bonding layer electrically connecting each of said second electrodes to that connection point corresponding to that second electrode.

15 5. The display of Claim 4 wherein said bonding layer comprises electrically conducting particles suspended in an electrically insulating adhesive.

20 6. The display of Claim 4 further comprising a light conversion layer in contact with said second surface of said flexible sheet, said light conversion layer absorbing light of said first wavelength and emitting light of a second wavelength.

25 7. The display of Claim 4 wherein said driving transistor are part of an array of thin film transistors.

30 8. A display comprising a plurality of light emitting pixels, said display comprising an array of driving transistor and an array of OLEDs, said array of OLEDs comprising:

a flexible sheet having first and second surfaces, said first and second surfaces being parallel to one another, said flexible sheet being transparent to light of a first wavelength;

a first electrode comprising a first electrode layer in contact with said first surface, said first electrode layer being transparent to light of said first wavelength;

a light emitting layer comprising an organic polymer in electrical contact with said first electrode layer; and

a plurality of second electrodes, one such second electrode corresponding to each of said OLEDs, each of said second electrodes comprising an isolated conducting area in contact with said light emitting layer, said light emitting layer generating light of said first wavelength in a region adjacent to said second electrode when a potential difference is applied across said first and second electrodes.

9. The display of Claim 8 wherein said array of driving transistor are part of a transistor array having a plurality of connection points disposed on a surface, each of said connection points corresponding to one of said second electrodes in said array of OLEDs, said connection points being arranged such that each second electrode overlies said corresponding connection point when said array of OLEDs is properly aligned with said transistor array, and wherein said display further comprises a bonding layer located between said transistor array and said array of OLEDs, said bonding layer electrically connecting each of said second electrodes to that second electrodes corresponding connection point.

10. The display of Claim 9 wherein said bonding layer comprises electrically conducting particles suspended in an electrically insulating adhesive.

11. The display of Claim 9 further comprising a light conversion layer in contact with said second surface of said flexible sheet, said light conversion layer absorbing light of said first wavelength and emitting light of a second wavelength.

12. The display of Claim 9 wherein said driving transistor are part of an array of thin film transistors.

Active Matrix Addressed Polymer LED Display

ABSTRACT

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A display having a plurality of light emitting pixels. Each pixels includes an isolation transistor, a driving circuit, and an organic light emitting diode (OLED). The driving circuit storing a value that determines the magnitude of the light emitted by that pixels, the driving circuit placing the OLED in a conducting path between the first and second power terminals.

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The driving circuit is programmed through the isolation transistor. In one embodiment of the present invention, the driving circuit includes a storage capacitor and a driving transistor. The OLEDs are part of an array of OLEDs. The array of OLEDs is constructed on a flexible sheet having first and second surfaces, the flexible sheet being transparent to light of a first

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wavelength. A transparent first electrode layer is in contact with the first surface. A light emitting layer including an organic polymer is in contact with the first electrode layer. A plurality of second electrodes, one such second electrode corresponding to each of the

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OLEDs, is in contact with the light emitting layer. Each second electrode has an isolated conducting area. The driving transistor are part of a transistor array having a plurality of connection points disposed on a surface, each connection point corresponding to one of the second electrodes in the array of OLEDs. The connection points are arranged such that each second electrode overlies the corresponding connection point when the array of OLEDs is properly aligned with the transistor array. The connection points are bonded to the corresponding second electrodes by a bonding layer.

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**DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION**ATTORNEY DOCKET NO. 10980239

As a below named inventor, I hereby declare that:

My residence/post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD OF MANUFACTURE OF ACTIVE MATRIX ADDRESSED POLYMER LED DISPLAY

the specification of which is attached hereto unless the following box is checked:

() was filed on _____ as US Application Serial No. or PCT International Application Number _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understood the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above. I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR 1.56.

Foreign Application(s) and/or Claim of Foreign Priority

I hereby claim foreign priority benefits under Title 35, United States Code Section 119 of any foreign application(s) for patent or inventor(s) certificate listed below and have also identified below any foreign application for patent or inventor(s) certificate having a filing date before that of the application on which priority is claimed:

COUNTRY	APPLICATION NUMBER	DATE FILED	PRIORITY CLAIMED UNDER 35 U.S.C. 119
			YES: _____ NO: _____
			YES: _____ NO: _____

Provisional Application

I hereby claim the benefit under Title 35, United States Code Section 119(e) of any United States provisional application(s) listed below:

APPLICATION SERIAL NUMBER	FILING DATE

U. S. Priority Claim

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION SERIAL NUMBER	FILING DATE	STATUS (patented/pending/abandoned)

POWER OF ATTORNEY:

As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) listed below to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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6-15-98

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Inventor's Signature Date

Full Name of # 7 joint inventor: _____ Citizenship: _____

Residence: _____

Post Office Address: _____

Inventor's Signature Date

Full Name of # 8 joint inventor: _____ Citizenship: _____

Residence: _____

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Inventor's Signature Date

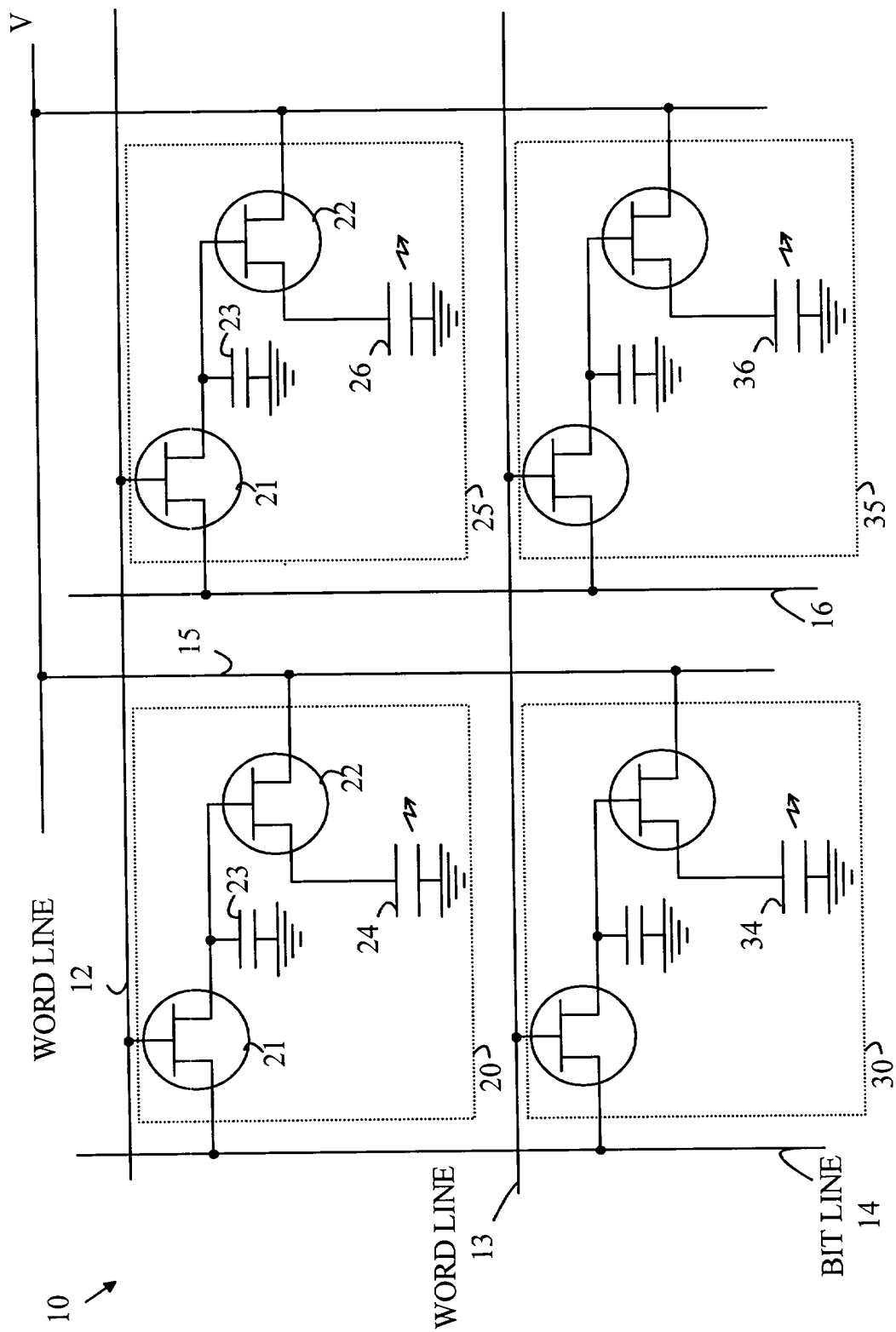


FIGURE 1

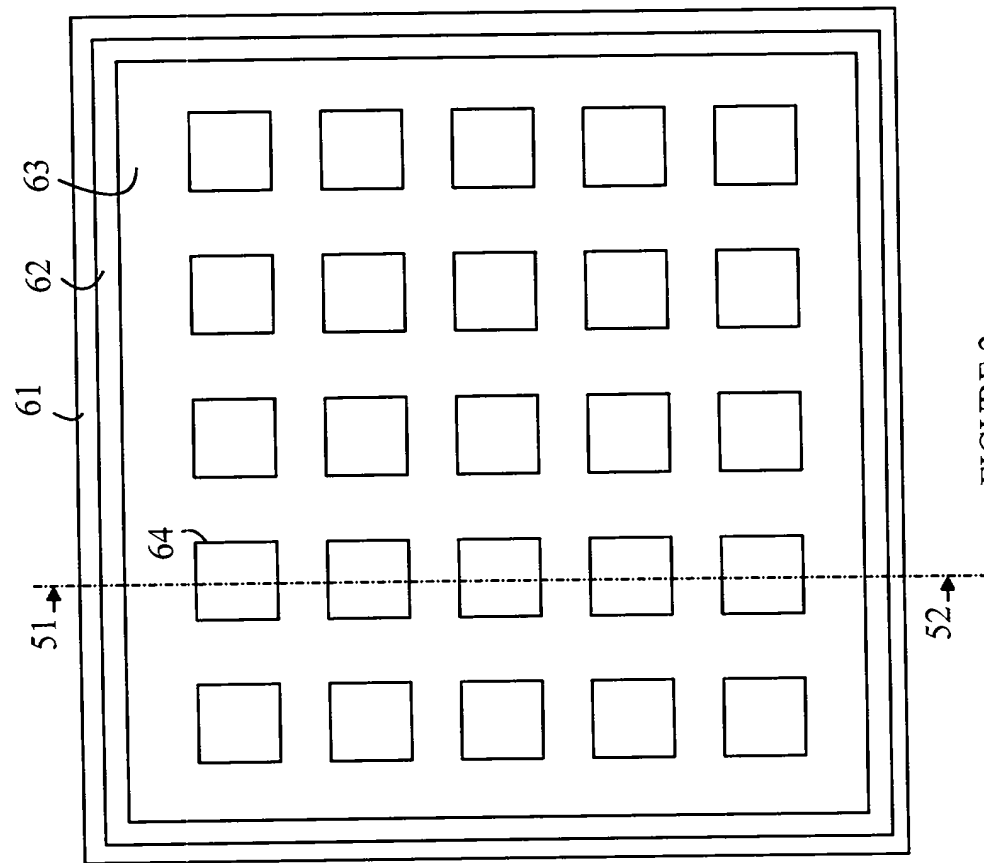


FIGURE 2

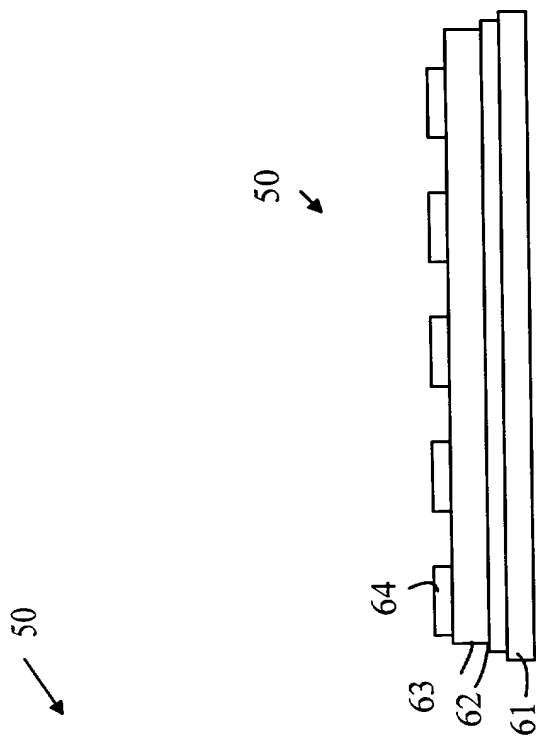


FIGURE 3

